



# i.MX 95 Applications Processor Family: High-Performance, Safety Enabled Platform with eIQ<sup>®</sup> Neutron NPU

## iMX95

### Preproduction

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information [contact support](#) or your sales representative.

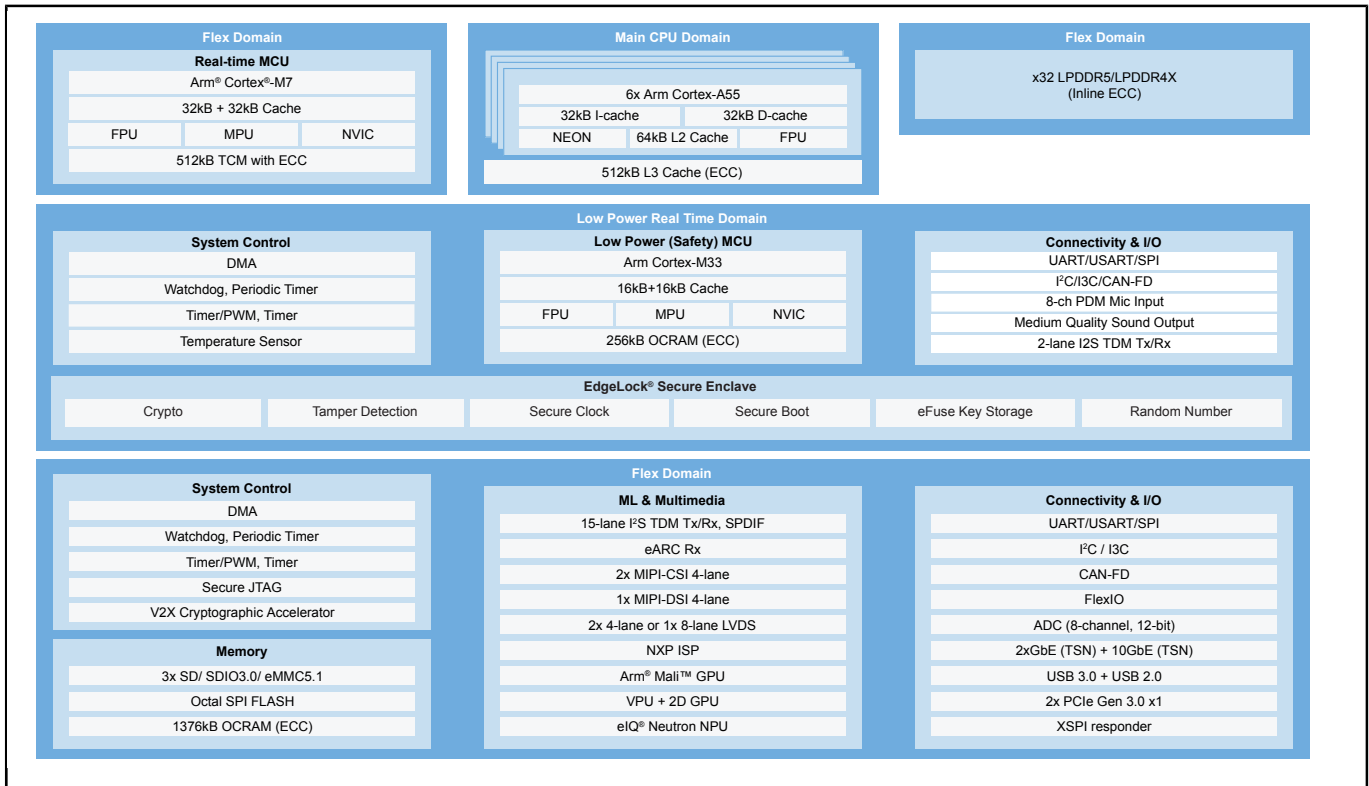
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The i.MX 95 applications processor family enables a broad range of edge applications from Automotive connectivity domain and eCockpit, to Industry 4.0 and IoT platforms. The family offers NXP SafeAssure<sup>®</sup> functional safety compliant platform development (ASIL-B, SIL2), 6x Arm<sup>®</sup> Cortex-A55 cores, Arm Mali GPU, 4K VPU, ISP, ML acceleration NPU, and Edgelock<sup>®</sup> secure enclave security.

The i.MX 95 family energy flex architecture provides scalable real-time, safety and application domains with high-speed connectivity (10GbE, USB 3, PCIe<sup>®</sup> Gen 3) to enable a wide range of platforms.

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# i.MX 95 Applications Processor Block Diagram



View additional information for [i.MX 95 Applications Processor Family: High-Performance, Safety Enabled Platform with eIQ® Neutron NPU](#).

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